

ZZZ1
PCB DAZ@
DA60010L000
PCB 10F LA-A391P REV0 M/B HDI

S1
FRAME
ECOMV000200

S4
FRAME
ECOMV000200
for GLONASS
@

Compal Confidential

Schematics Document

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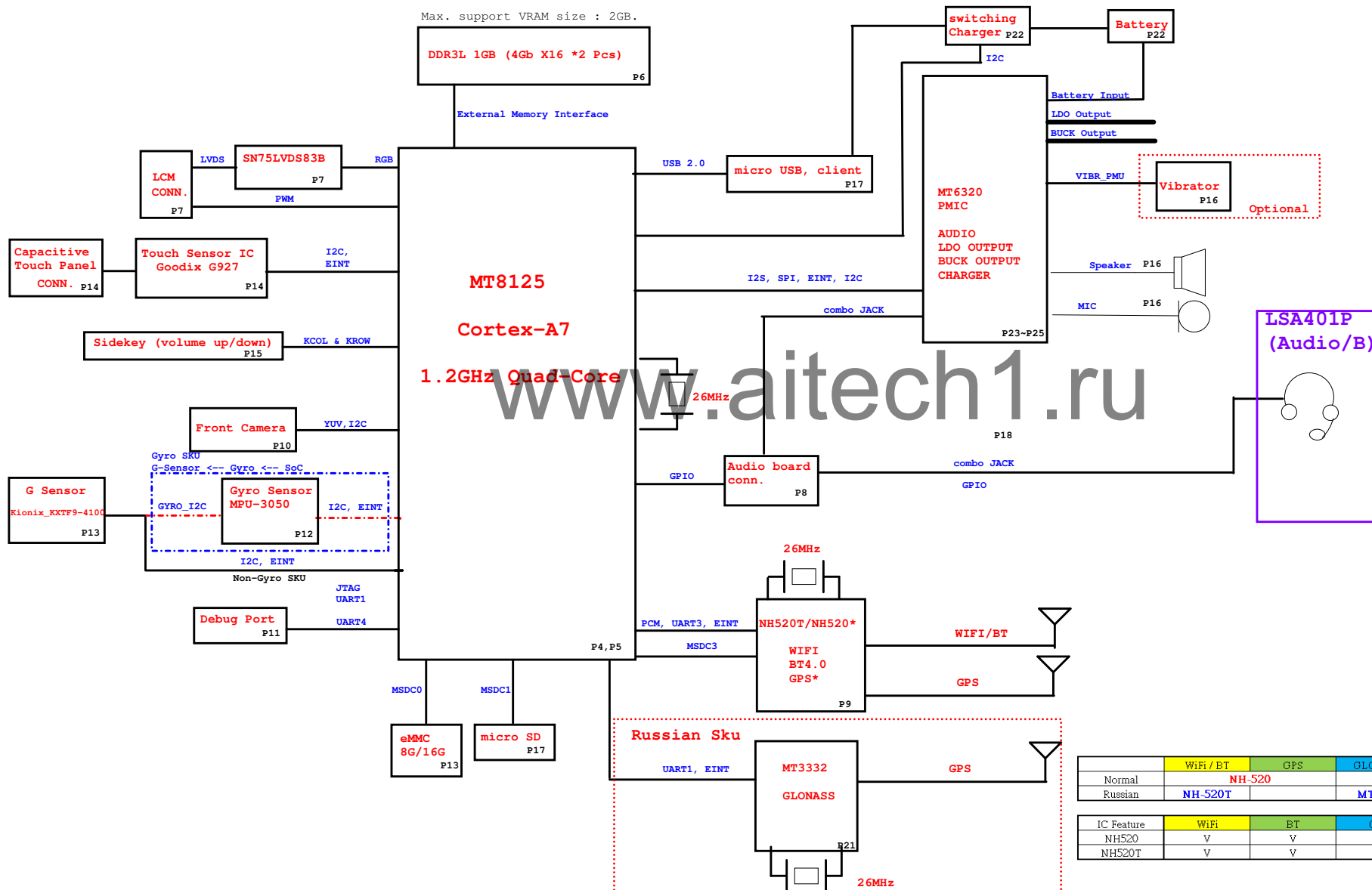
LA-A391P

2013-03-13

REV: 0.1

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Issued Date	2012/11/09	Deciphered Date	2014/11/09	Title Cover Page	
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giordano2 WIFI SKU block diagram



	WiFi / BT	GPS	GLONASS
Normal	NH-520		
Russian	NH-520T		MT3332

IC Feature	WiFi	BT	GPS	FM
NH520	V	V	V	V (No support)
NH520T	V	V	V	V (No support)

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Voltage Rails

MT6320 Power Plane	Function	Power Level	IDLE	Sleep mode
VCORE_PMU	VCORE switching output	0.75 ~ 1.3	ON	Low voltage
VPROC_PMU	VPROC switching output	0.75 ~ 1.3	ON	Low voltage
VTCXO_1_PMU	LDO output for TCXO	2.8V	ON	OFF
VCAMA_PMU	LDO output for camaera analog	2.8V	ON	Gating by SW
VSRAM_PMU	LDO output used for 1.2V SRAM	1.2V	ON	Low voltage
VDD28_6583	LDO output used for 2.8V IO	2.8V	ON	Gating by SW
VGP2_PMU	LDO output for camaera 1.8V	1.8V	ON	Gating by SW
VEMC_3V3_PMU	LDO output for eMMC	3.3V	ON	Gating by SW
VMCH_PMU	LDO output for SD card	3.3V	ON	Gating by SW
VGP5_PMU	LDO output for Touch panel	2.8V	ON	Gating by SW
DDR3VCCIO	LDO output for DDR3L	1.35V	ON	Gating by SW
VDD33_6583	LCD & Bridge power	3.3V	ON	Gating by SW
VDD18_6583	LDO output used for 1.8V IO	1.8V	ON	Gating by SW

I2C address

Device	Address(7 bit)	Address(8bit)	
		Write	Read
Gyro (MPU-3050)	0x68	0xD0	0xD1
G-sensor (KXTIK-1004)	0x0F	0x1E	0x1F
Touch screen (GT927)	0x5D	0xBA	0xBB
Camera (OV7675)	0x21	0x42	0x43
Charge (BQ24196)	0x6B	0xD6	0xD7
G-sensor(STK-8313)			

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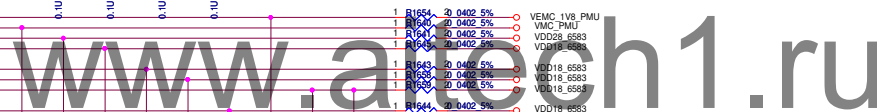
Main board ID

MB_ID0	MB_ID1	
0	0	EVT
0	1	DVT
1	0	PVT
1	1	MP

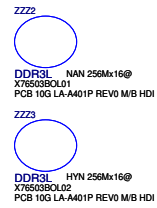
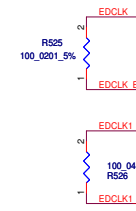
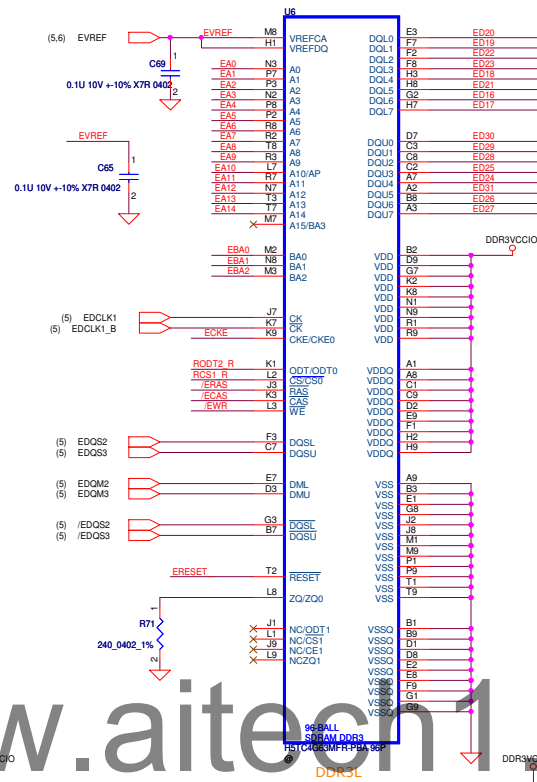
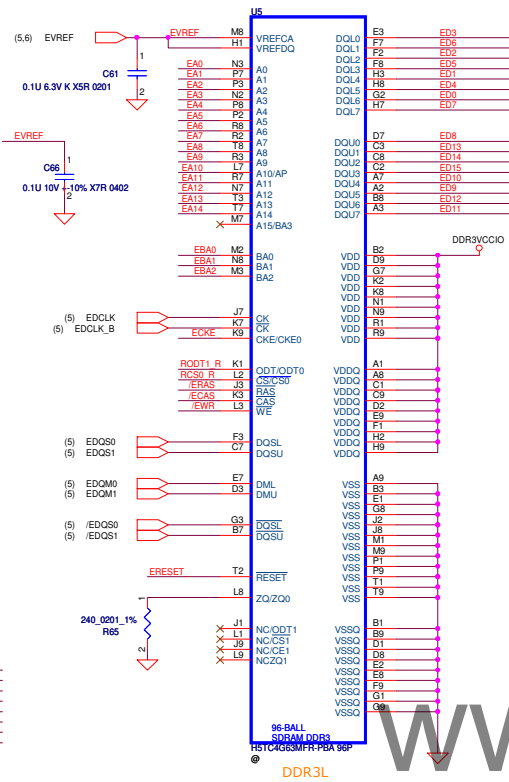
BOM structure

Name	Function	
DAZ@	PCB	
EMC@	for EMC request	
NH520@	AW-NH520	
NH520_EMC@	GPS EMC	
GLONASS@	MT3332	

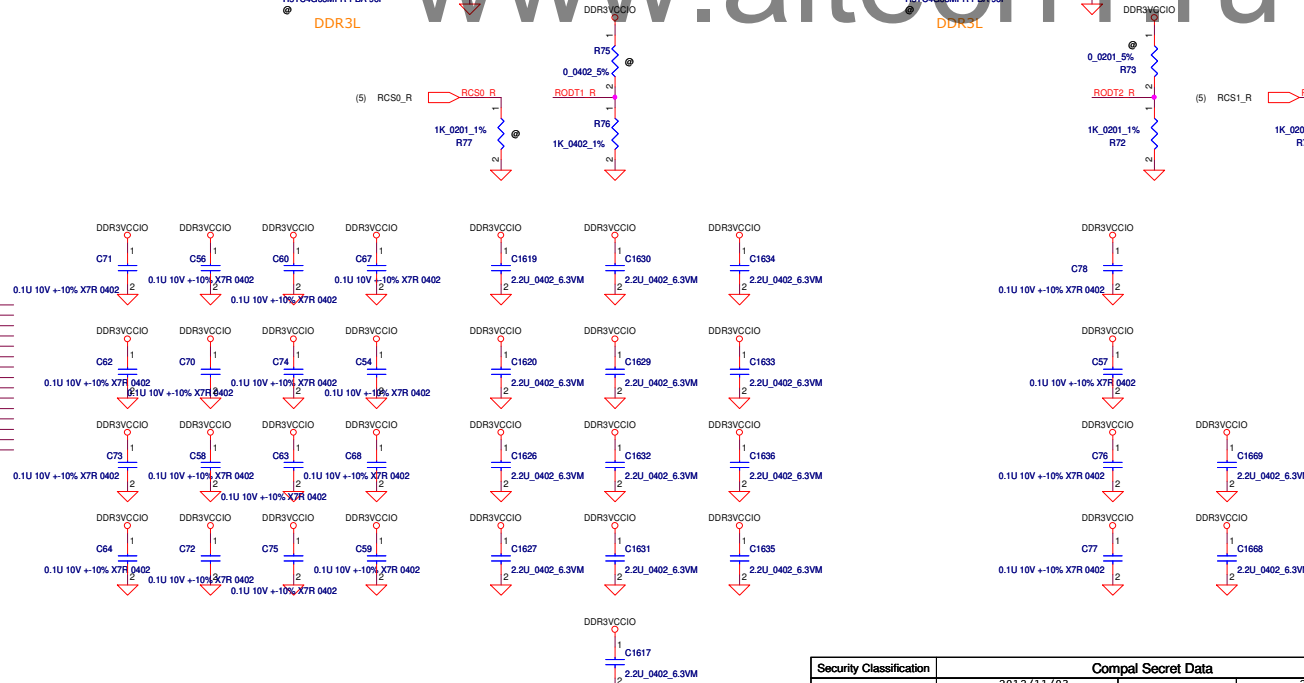
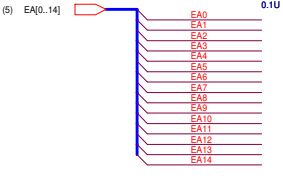
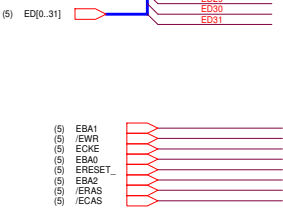
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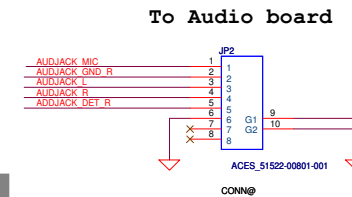
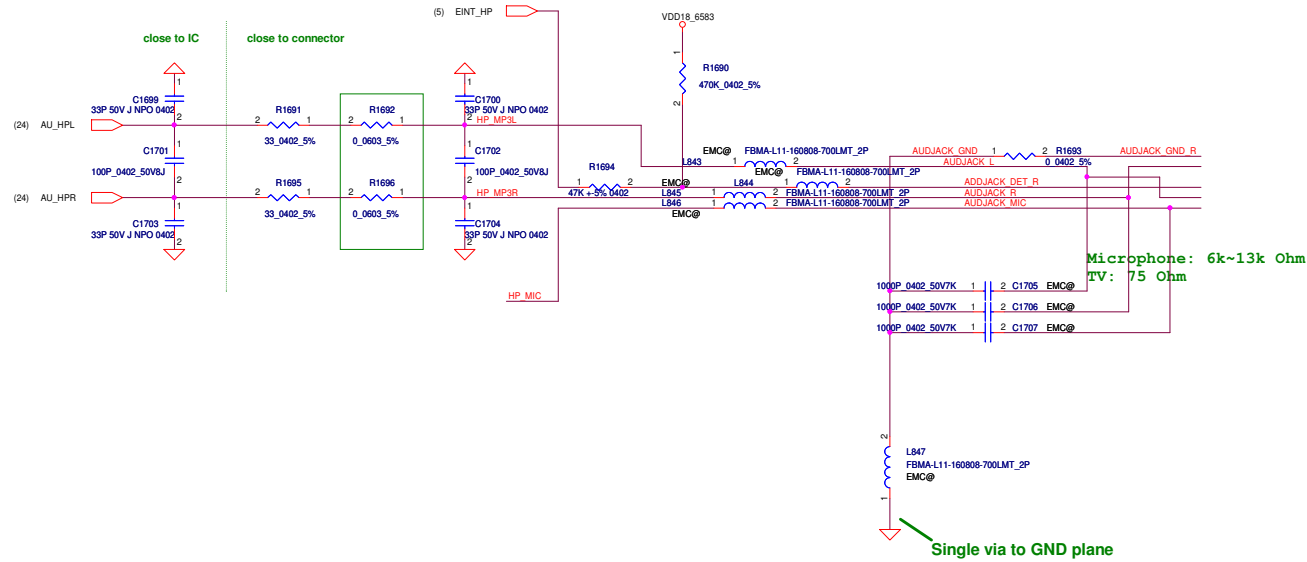
Vendor	X76 level	BOM structure
NANYA-1G	X76503BOL01	NAN 256Mx16@
HYNIX-1G	X76503BOL02	HYN 256Mx16@



U5 Test Point	
TP01 PAD @	ERAS
TP02 PAD @	ECAS
TP03 PAD @	EWRR
TP04 PAD @	EB0
TP05 PAD @	EB0
TP06 PAD @	EB0
TP07 PAD @	EDQM
TP08 PAD @	EDQS
TP09 PAD @	EDQS
TP10 PAD @	EDQS
TP11 PAD @	EDQS
TP12 PAD @	EDQS
TP13 PAD @	EA0
TP14 PAD @	EA0
TP15 PAD @	EA0
TP16 PAD @	EA0
TP17 PAD @	EA0
TP18 PAD @	EA0

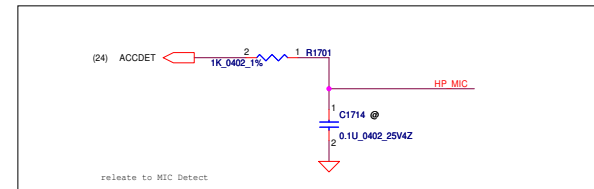
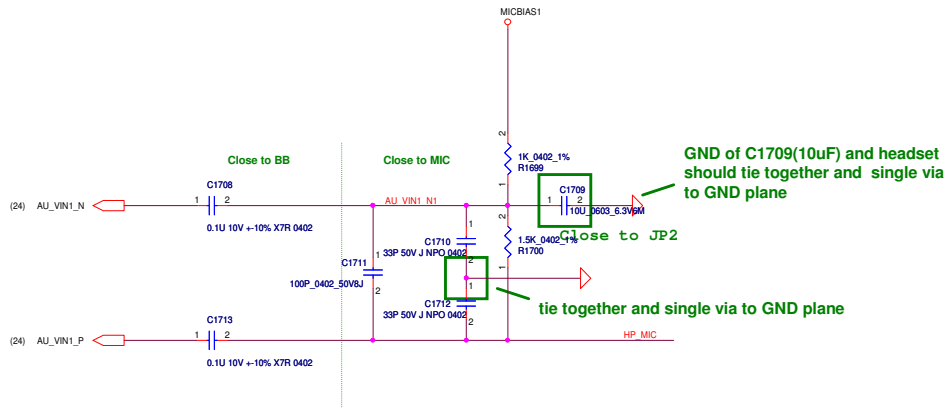
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TP20 PAD @	ECAS
TP21 PAD @	EWRR
TP22 PAD @	EB0
TP23 PAD @	EB0
TP24 PAD @	EB0
TP25 PAD @	EDQM
TP26 PAD @	EDQS
TP27 PAD @	EDQS
TP28 PAD @	EDQS
TP29 PAD @	EDQS
TP30 PAD @	EA0
TP31 PAD @	EA0
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TP34 PAD @	EA0
TP35 PAD @	EA0
TP36 PAD @	EA0

Earphone RECEIVER



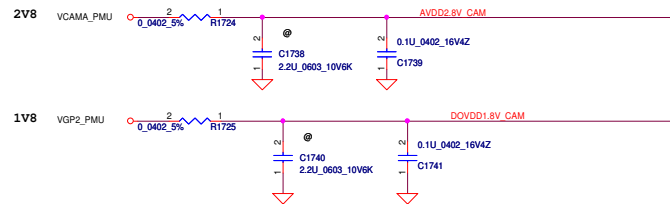
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Earphone MICPHONE

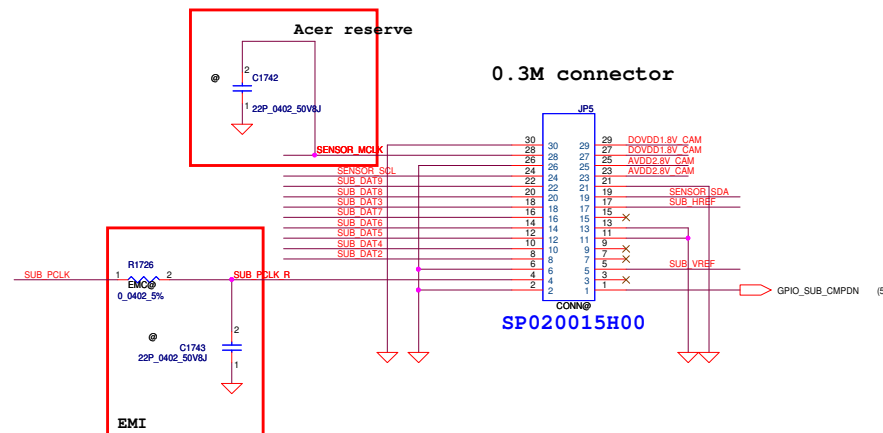


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Sub Camera

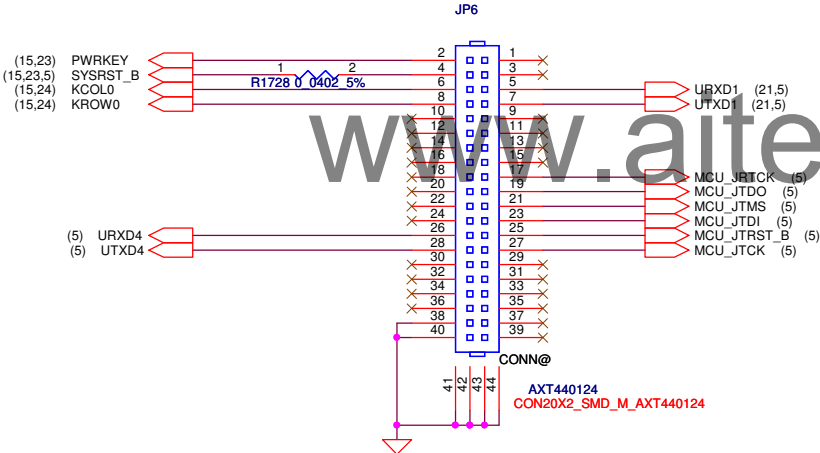


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Common Debug

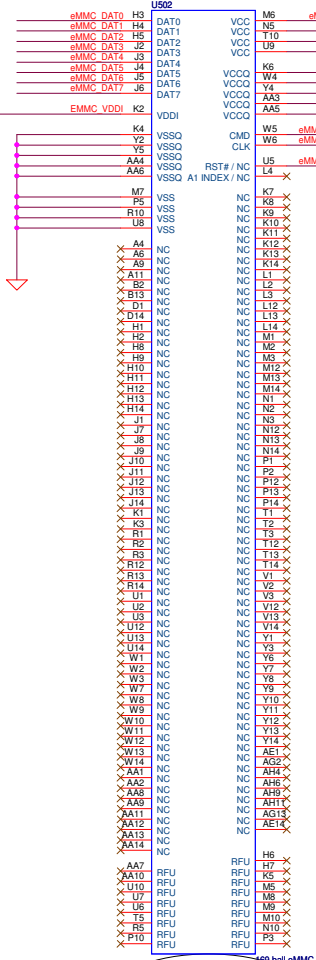
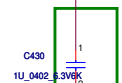


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169 ball eMMC

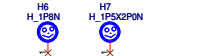
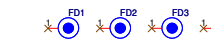
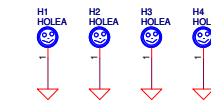
Close to
Memory



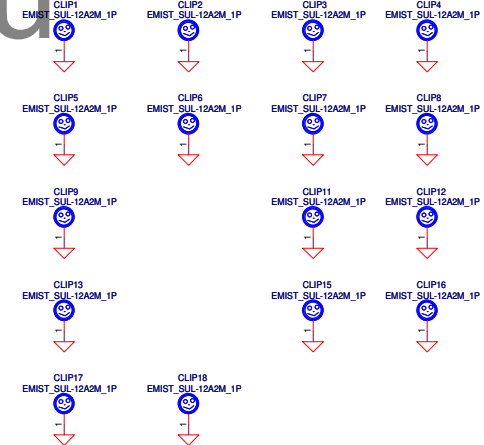
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© ompal footpint



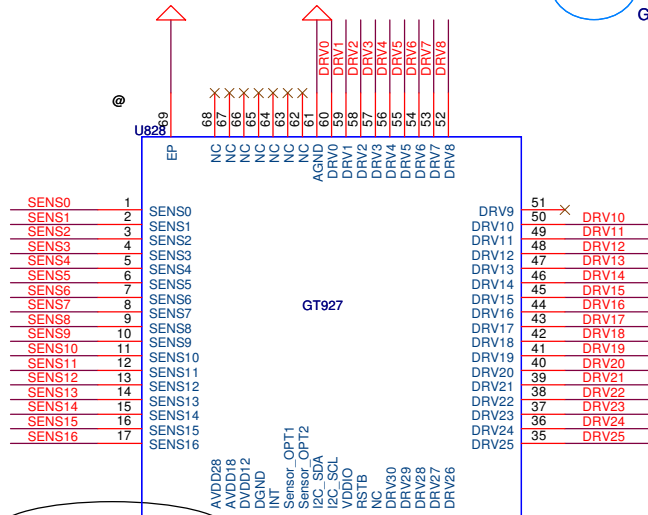
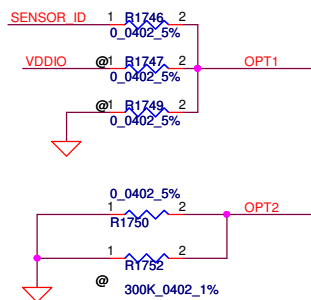
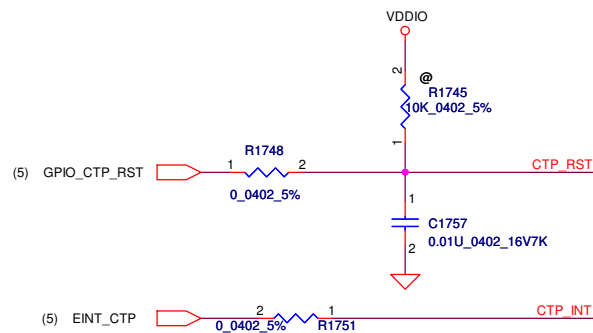
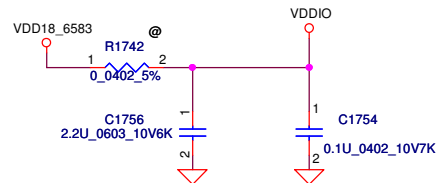
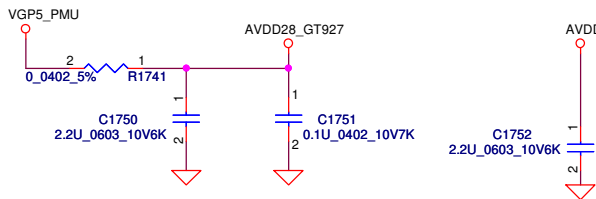
SCREW HOLE



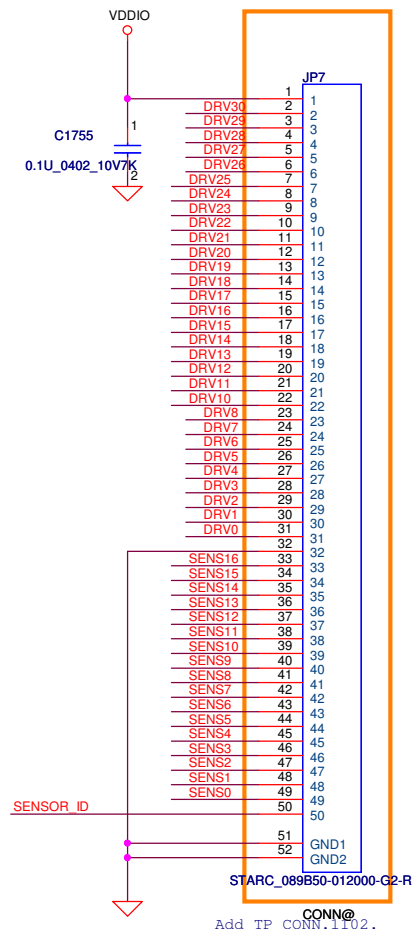
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Kingston 8G KE4CN3K6A FBGA 169P
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- U502 HYN 8GB@
SA00006V110
HYNIX 8G H26M42003GMR FBGA 153P
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HYNIX 16G H26M52003EQR FBGA 169P



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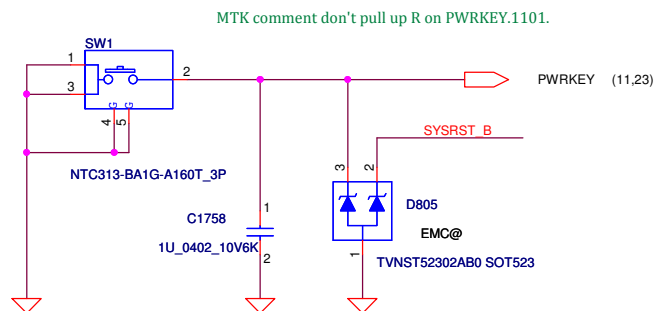
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GT927 QFN 68P TOUCH



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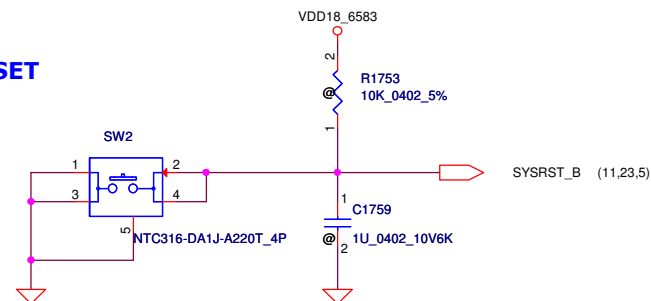
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Power-on Button

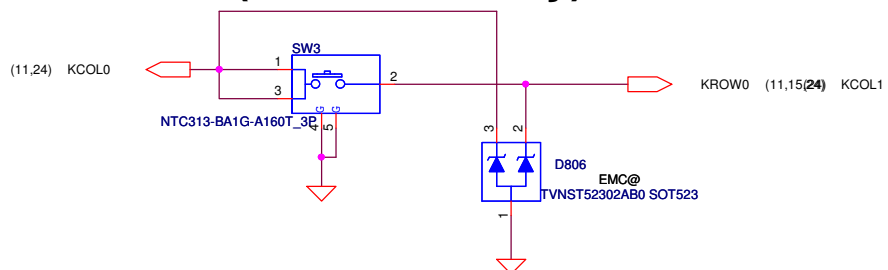


Compal GND MTK GND

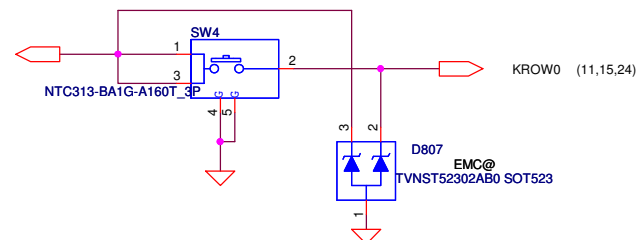
RESET



Volume Up (Download Key)

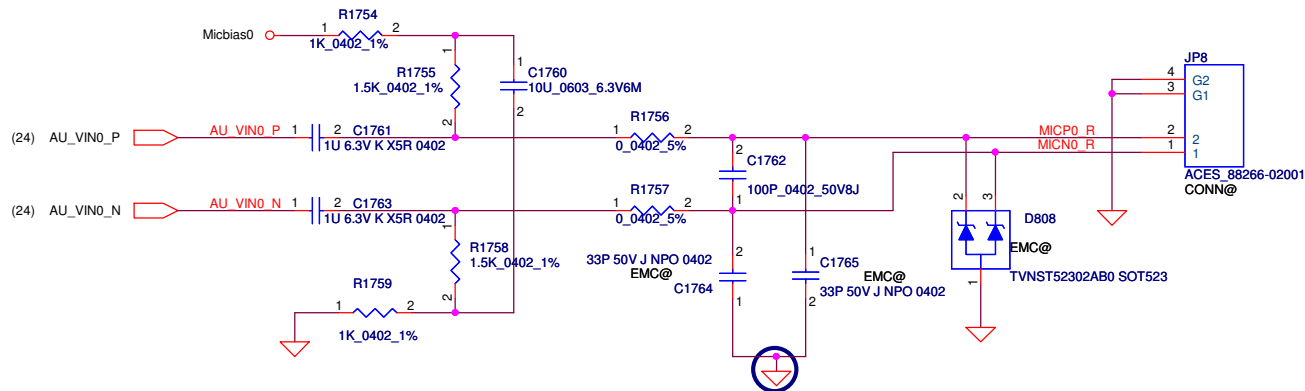


Volume Down



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MIC

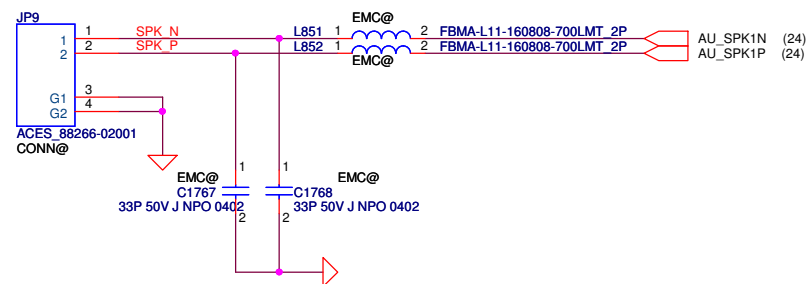


Modify MIC CONN SCH.1102.

Compal GND

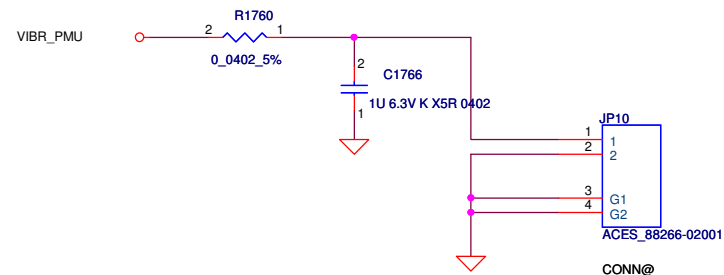
MTK GND

SPAKER



VIBR_PMU

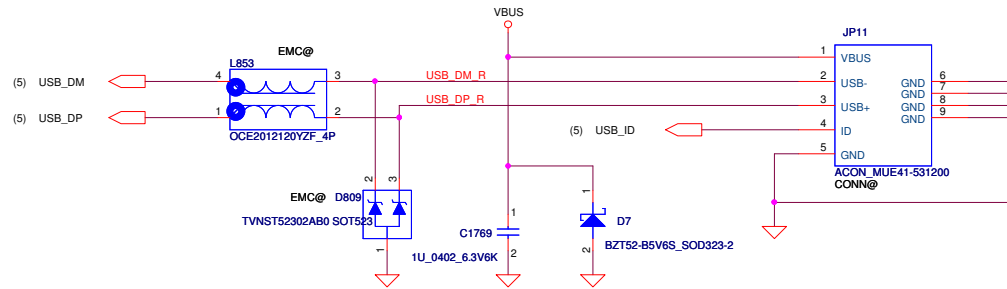
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Micro-USB

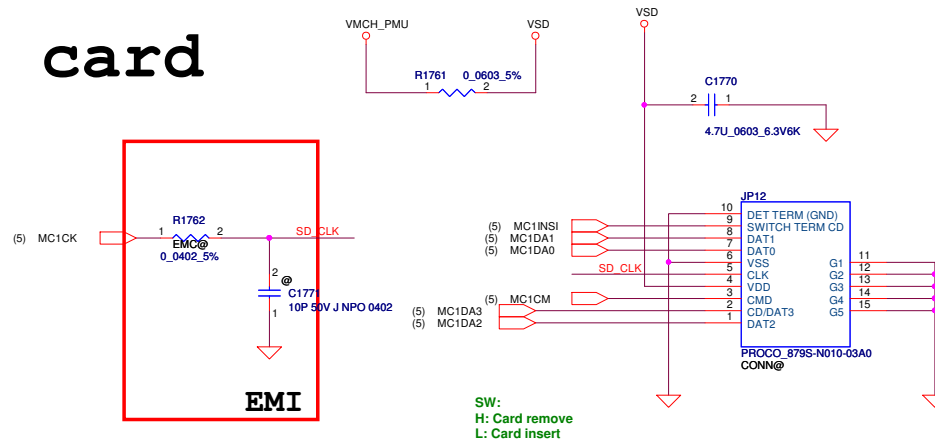
If mini-A connector insert => CID < 0V => Low
If mini-B connector insert => CID > 1.2V => High
IDPULLUP pin is replaced by 1.2V power source.



Compal GND MTK GND

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SD card



EMI

SW:
H: Card remove
L: Card insert

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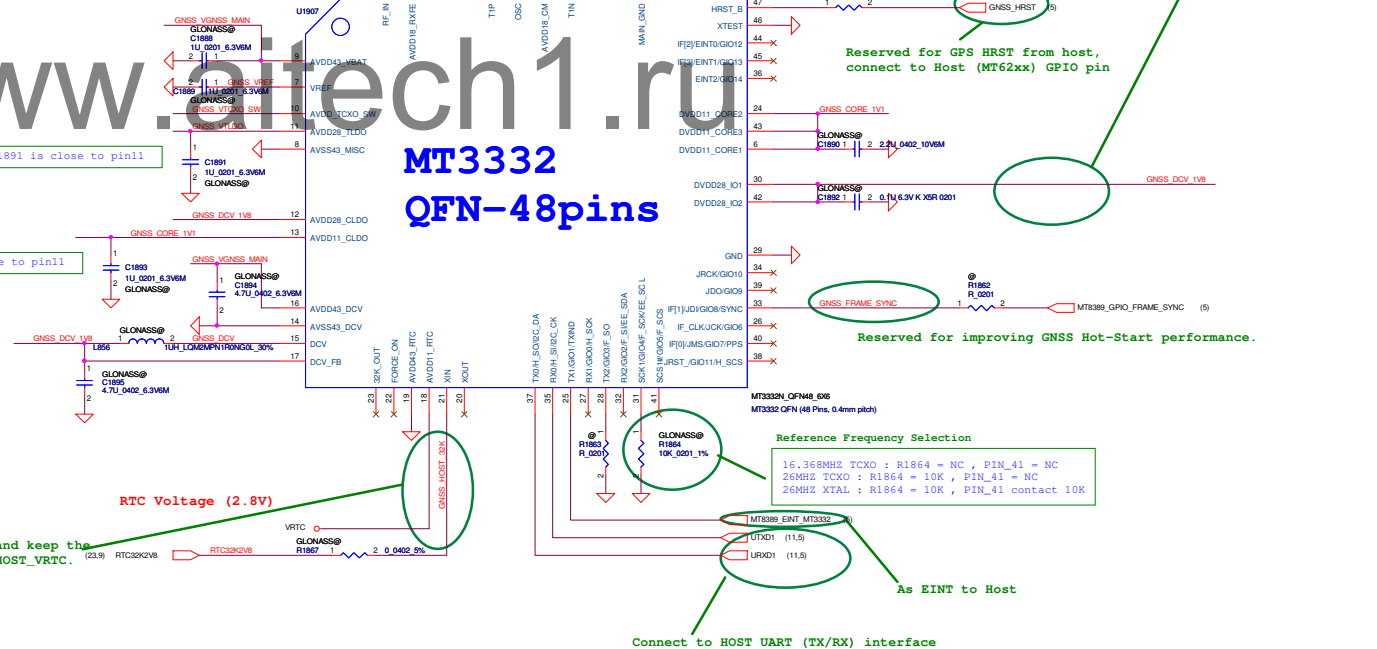
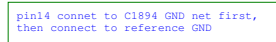
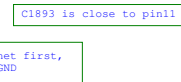
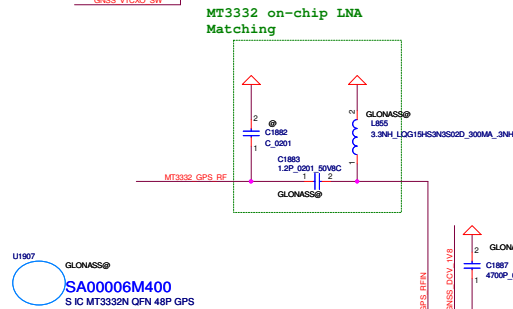
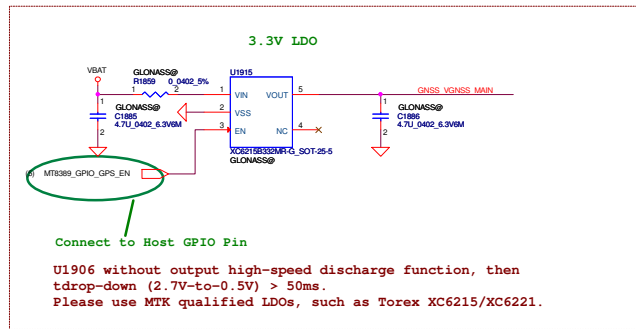
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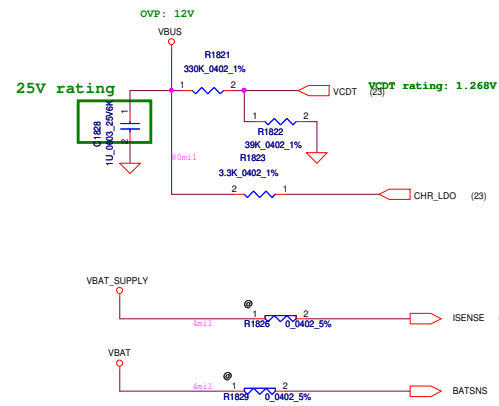
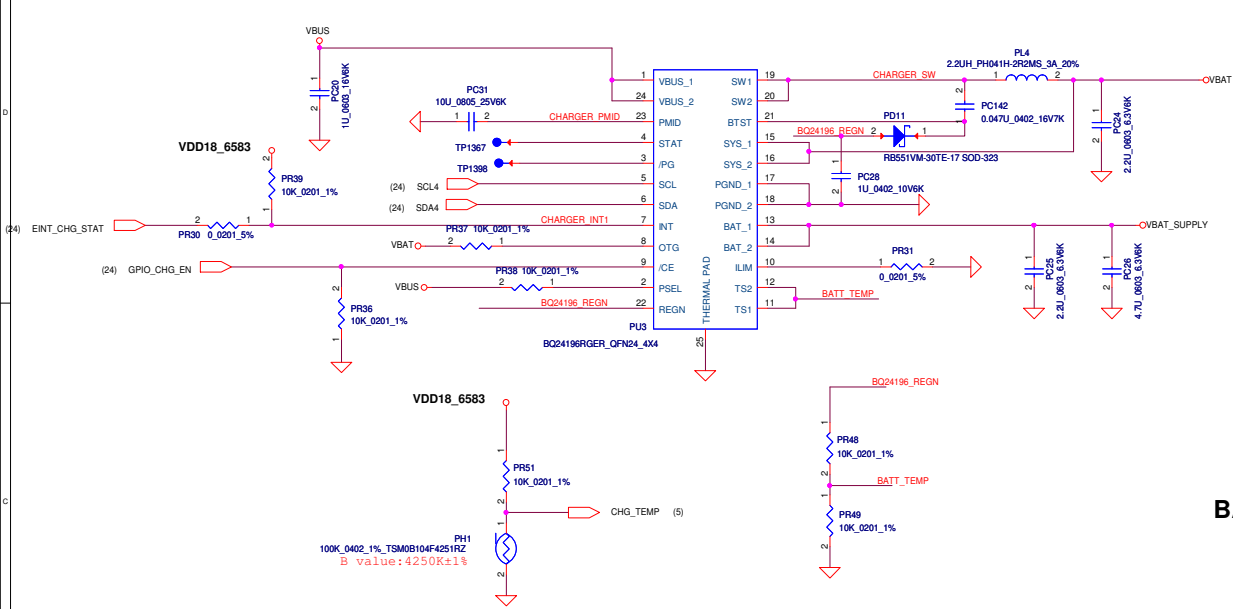
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Connect GNSS_HOST_VRTC to always alive voltage source, and keep the voltage swing of GNSS_HOST_32K RTC clock same as GNSS_HOST_VRTC.

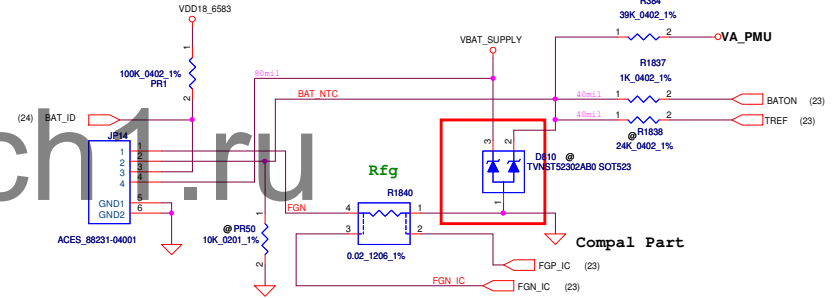
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Charger



BATTERY CONNECTOR

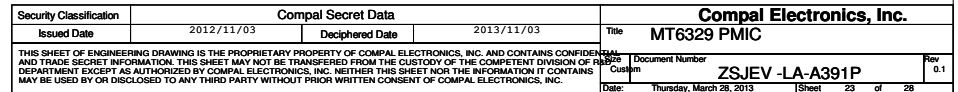
Add BAT_CONN.1102.



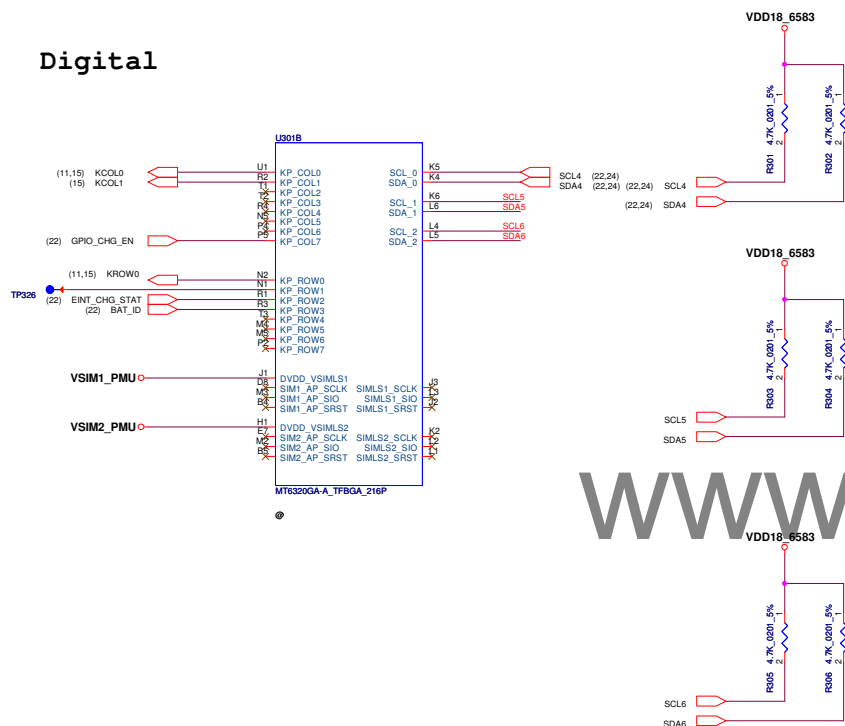
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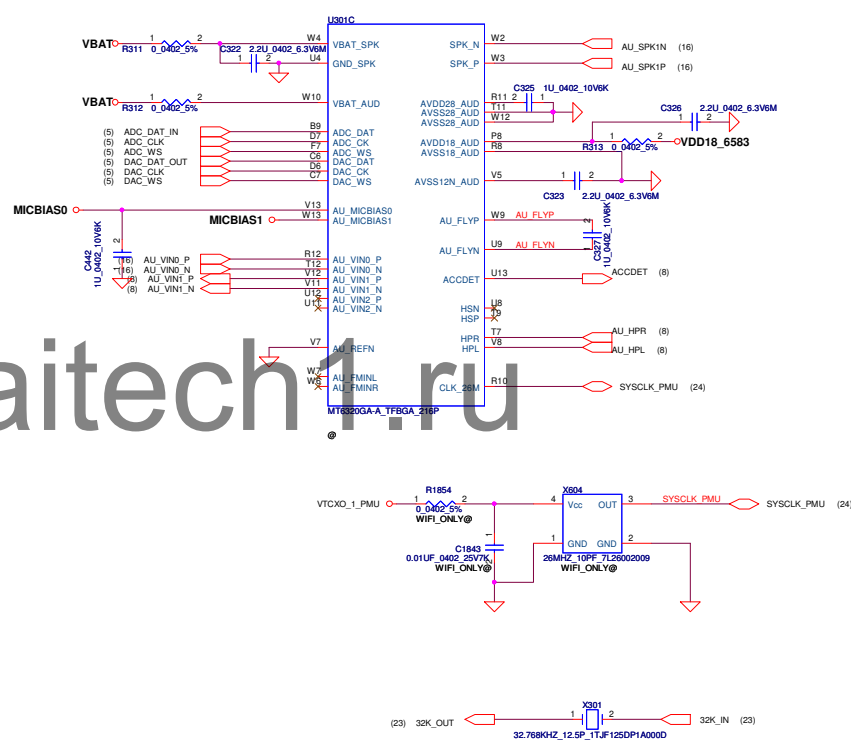
	LPDDR2/1.2V	DDR3U/1.25V	DDR3L/1.35V	DDR3/1.5V
VMSEL1	L	L	H	H
VMSEL2	L	H	L	H

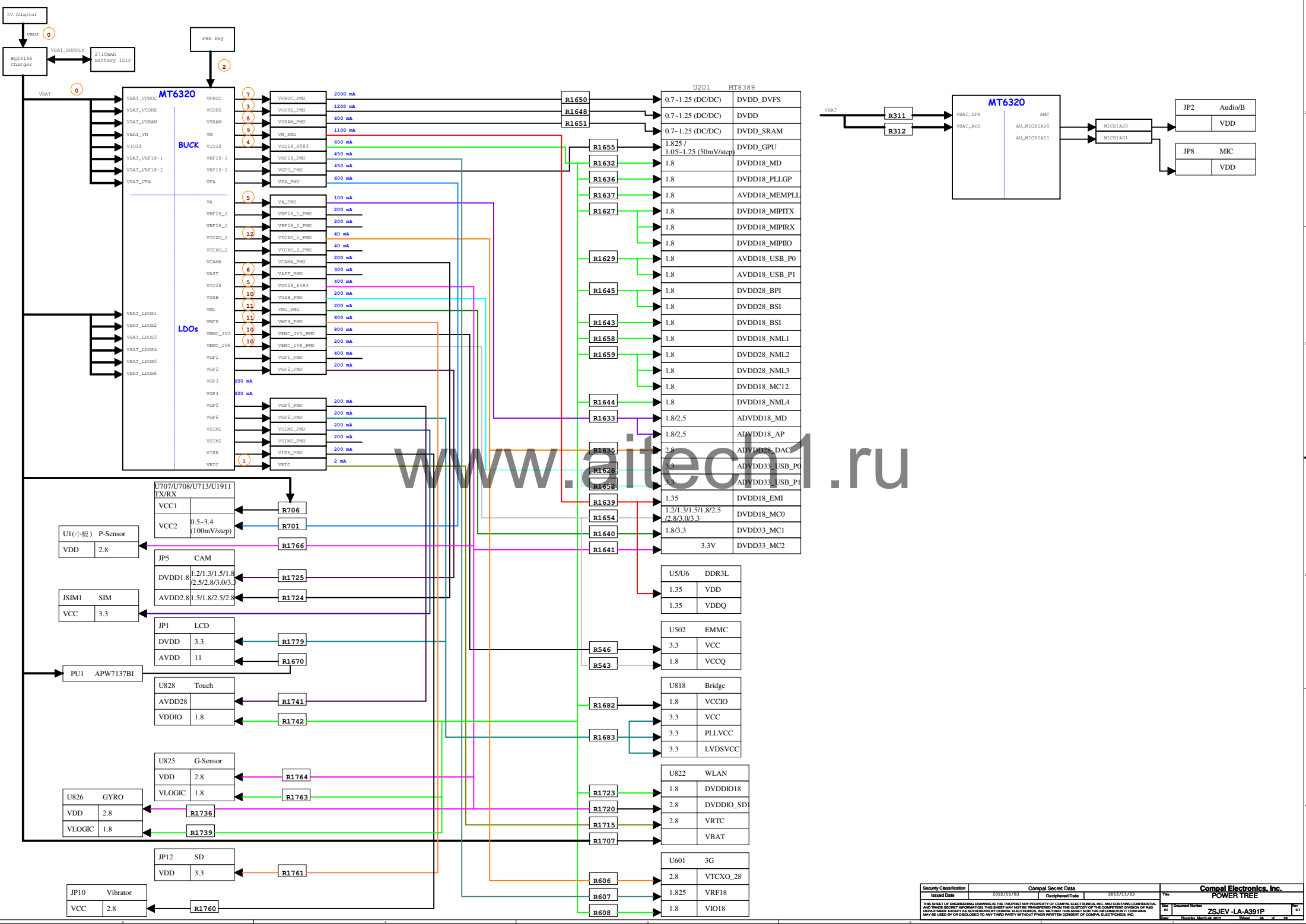


Digital



Audio





2013/3/11
Update PD11 , D308 , D309 , D310 , D311 , L301 ,
L302 , L303 , L304 , L307 , C647 , C646 , C635 ,
C638 compal P/N
Change to un-pop : PR50 , R1838 , R314 ,R315
Change to pop : R1855

2013/3/22
Add C318
Del R332,R348

2013/3/25
Delete TP324, TP325, TP327, TP328 test point

2013/3/28
Delete net SIM1_SCLK, SIM1_SIO, SIM1_SRST,
SIM1_CLK, SIM1_DATA, SIM1_RST

2013/3/29
R1847 change to 0-ohm

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2013/3/13
ADD NH520@,NH520_EMC@
modify RAM BOM structure
P01 Add S4 for GLONASS frame
P04. R1639 to 0_R_0805
R1648,R1650 to 0_R_0603
Del R1653
P05. Del TP1373,R634,R1765,R1766
Del U201A no function net
P06. C70,C64,C68 to 0.luf_0402
P07 R1675,R1678 to @
P08. Del R213, net POUT
JP2_FIN8 to NC
P09. update U823 symbol
P13. R535 to 27ohm(MTK advice)
P15. R1753,C1759 to @ (MTK advice)

2013/3/22
ADD S4,S5
L838,L839,L840,L841,L842,L853 --> SM070001R00
update EMMC PN.
P05. ADD TP1366,TP1369
R1520 to GLONASS@, R1521 to NH520@
P10. C1743 to @, R1726 to 0 ohm for EMI reserve
P13. DEL CILP19,CLIP20
P16. C1766,R1760 del @
P17. C1771 to @, R1762 to 0 ohm for EMI reserve
P21. C1887,C1902-->SE000000WN00
C1880,C1884,C1888,C1889,C1891,C1893-->SE000000UC00
C1890-->SE000000V600
R1859 to U1915 PIN1

2013/3/26
P06. R72,R73,R65,R70,R525,R77 to 0201 size

2013/3/26A
P06. C61 to 0201 size

2013/3/28
P15. SW1,3,4 co-layout SN100008300

2013/3/29
P04. R1650 change to short pad
P05. del net: SIM1_SCLK, SIM1_SIO, SIM1_SRST, EINT_A

2013/ 4/ 1
P15. del co-layout switch button(SW5,6,7)

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